

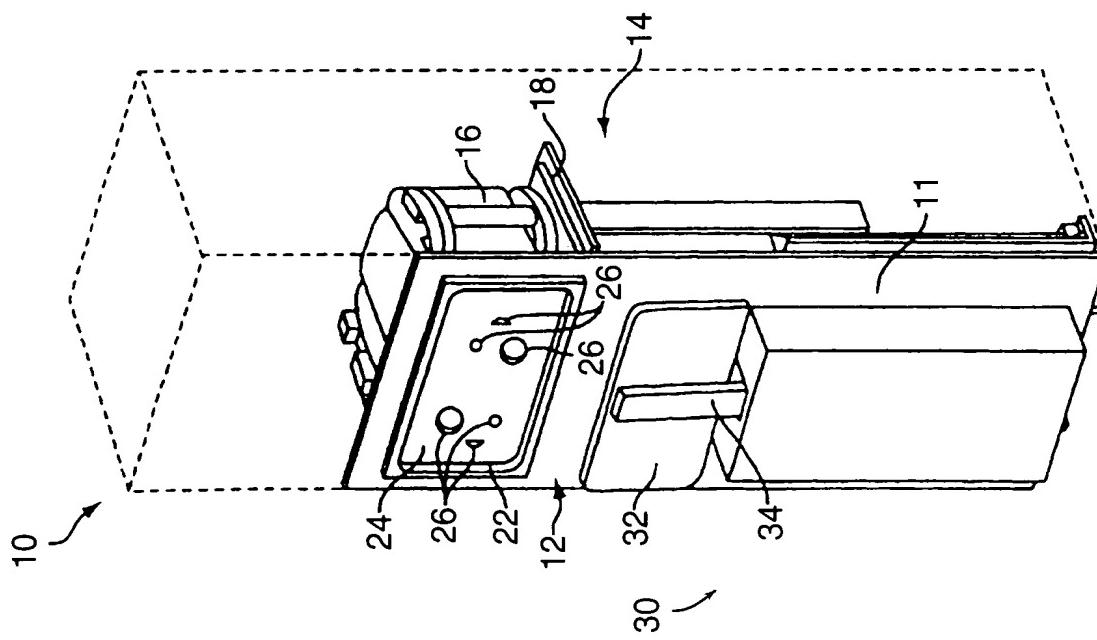
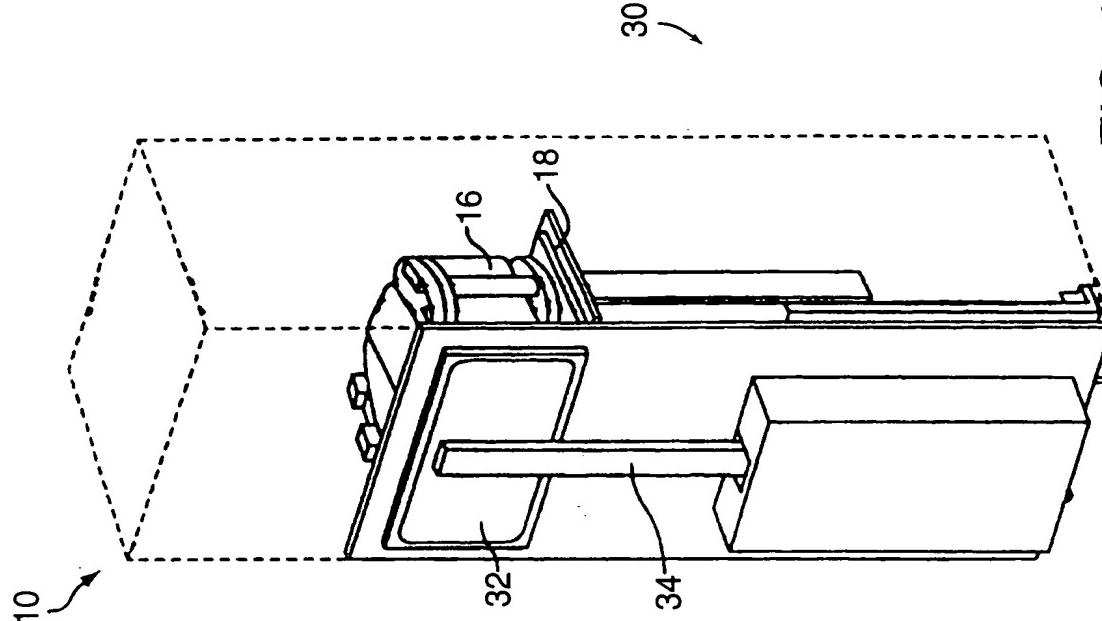
APPROVED	O.G. FIG.
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Wafer Processing Equipment  
Inventor: Soucy et al.  
Serial No. 09/897,820  
Atty Docket No. APE-001  
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**FIG. 1B**  
PRIOR ART



**FIG. 1A**  
PRIOR ART

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FIG. 1D  
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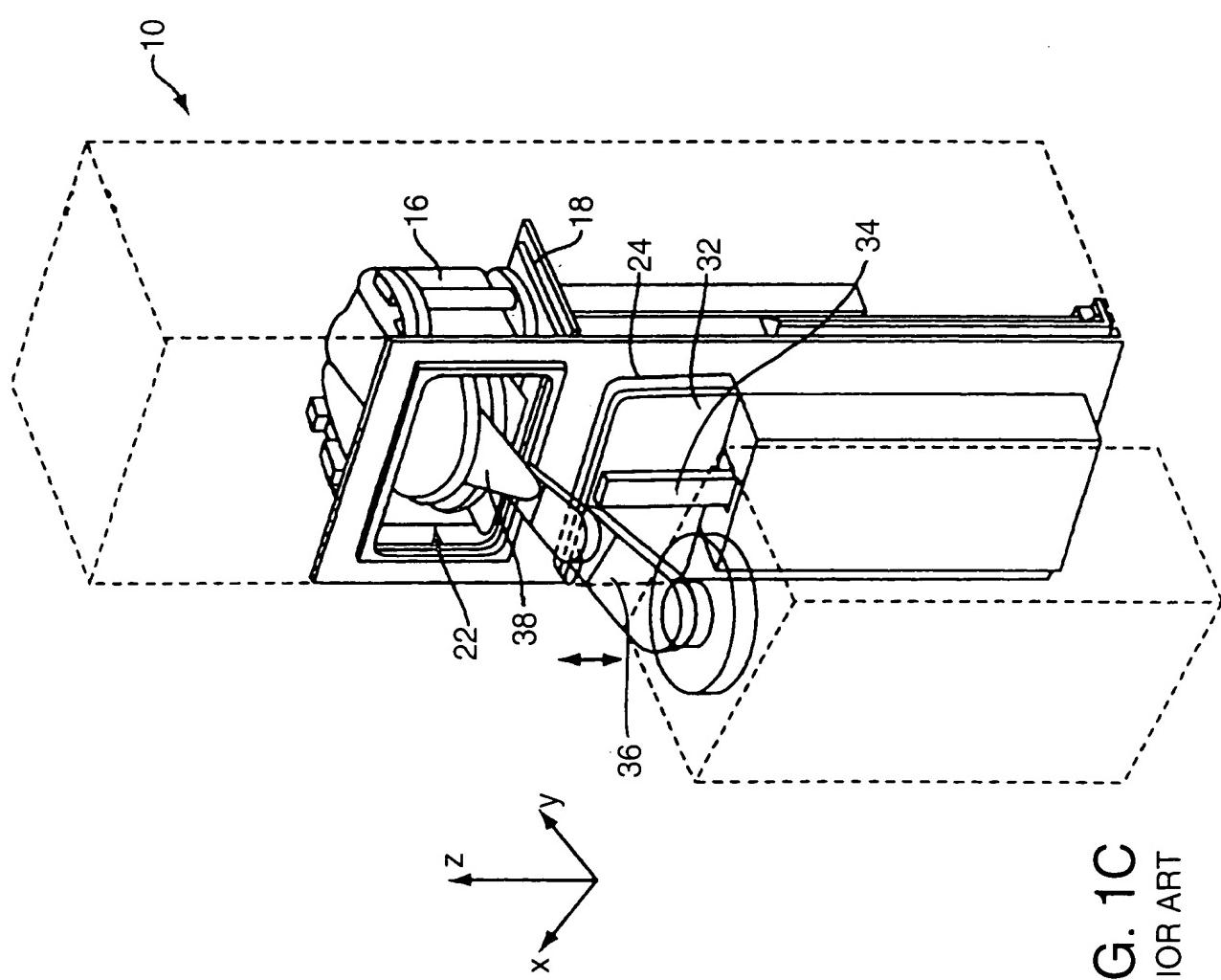
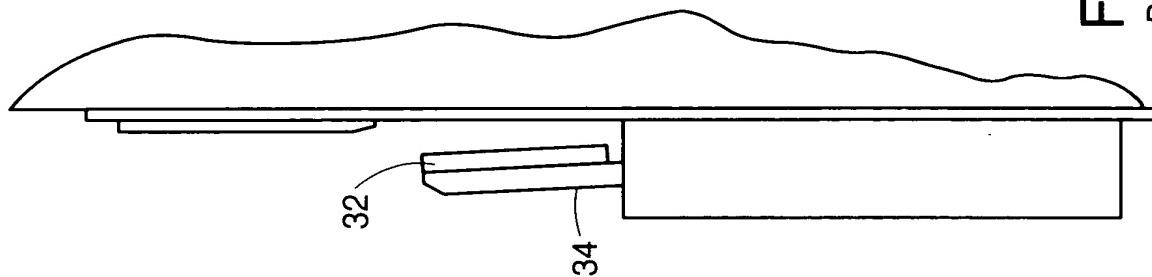


FIG. 1C  
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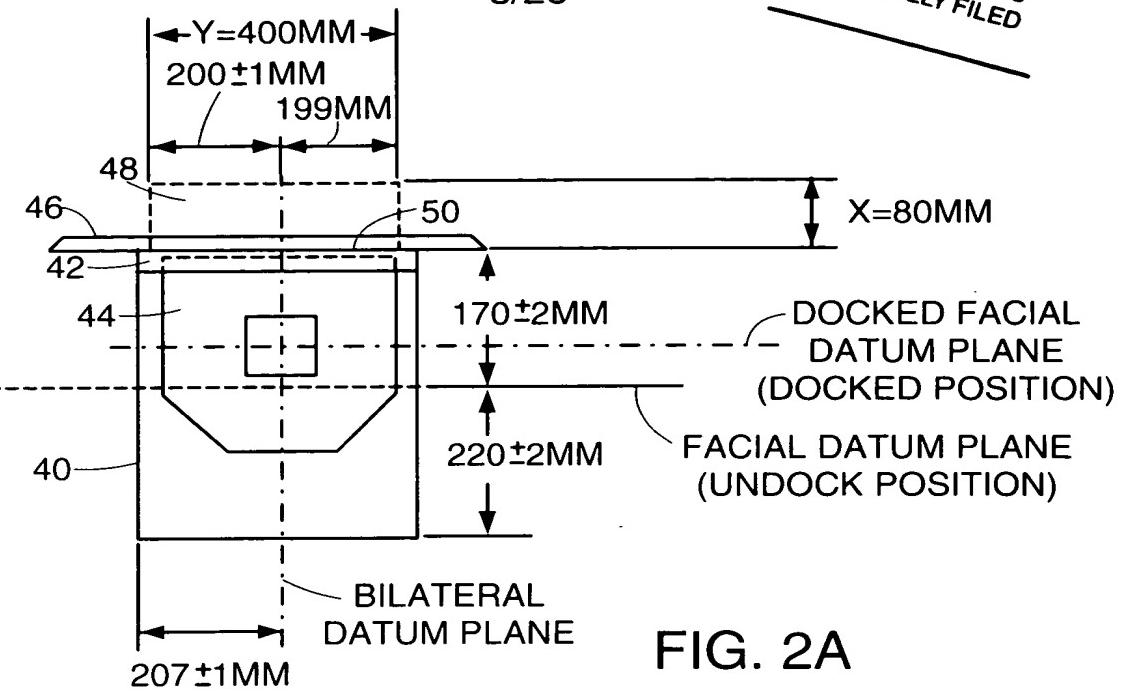


FIG. 2A

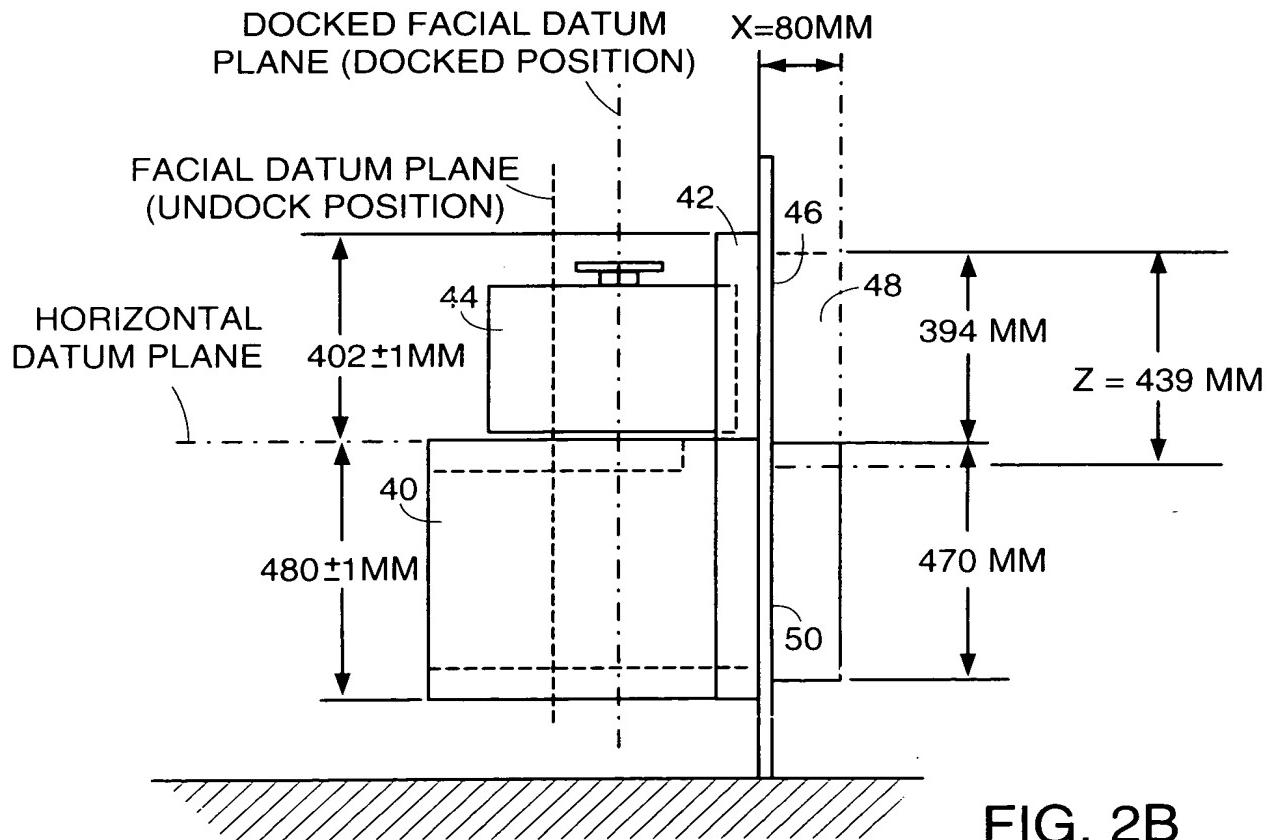


FIG. 2B

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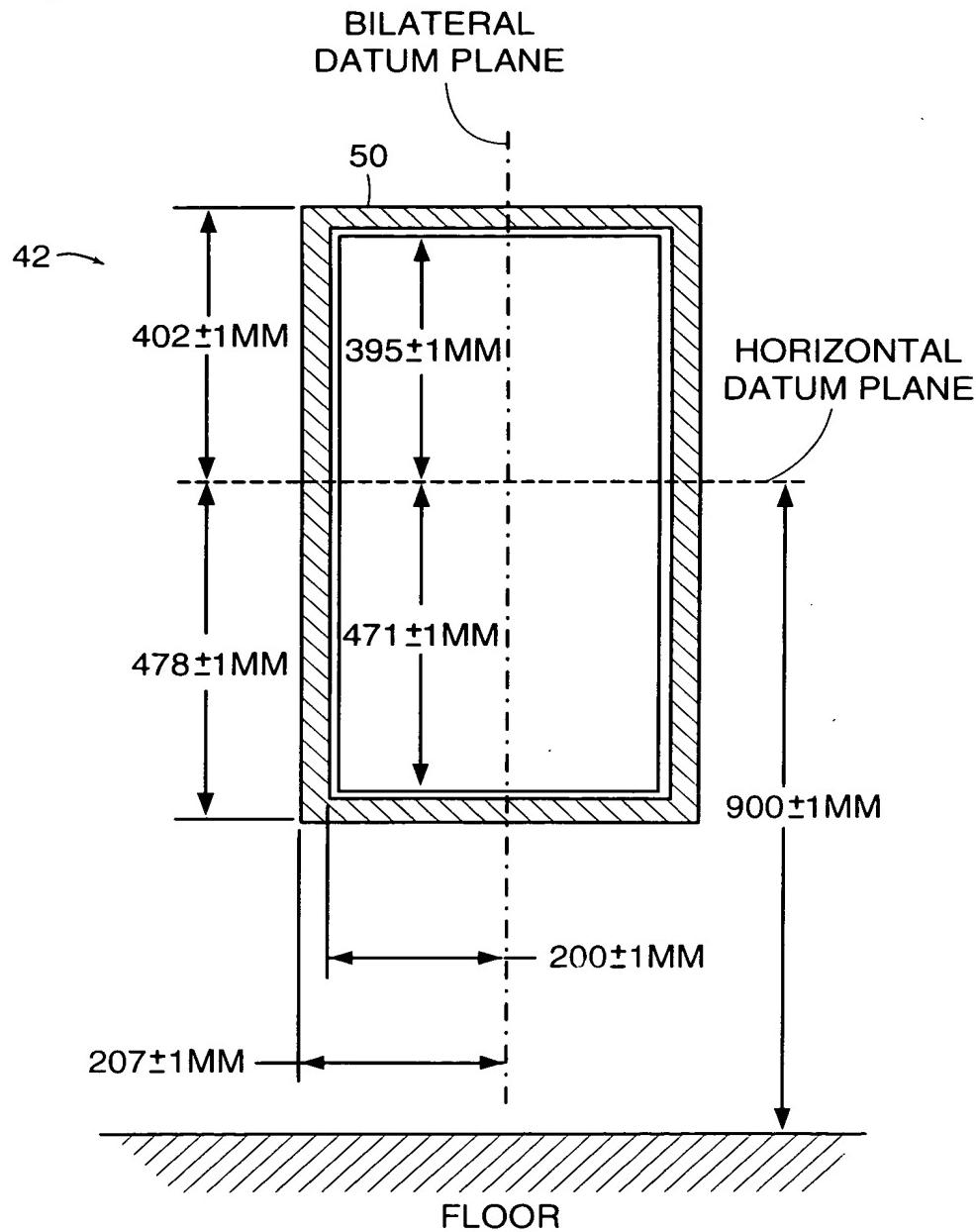


FIG. 2C

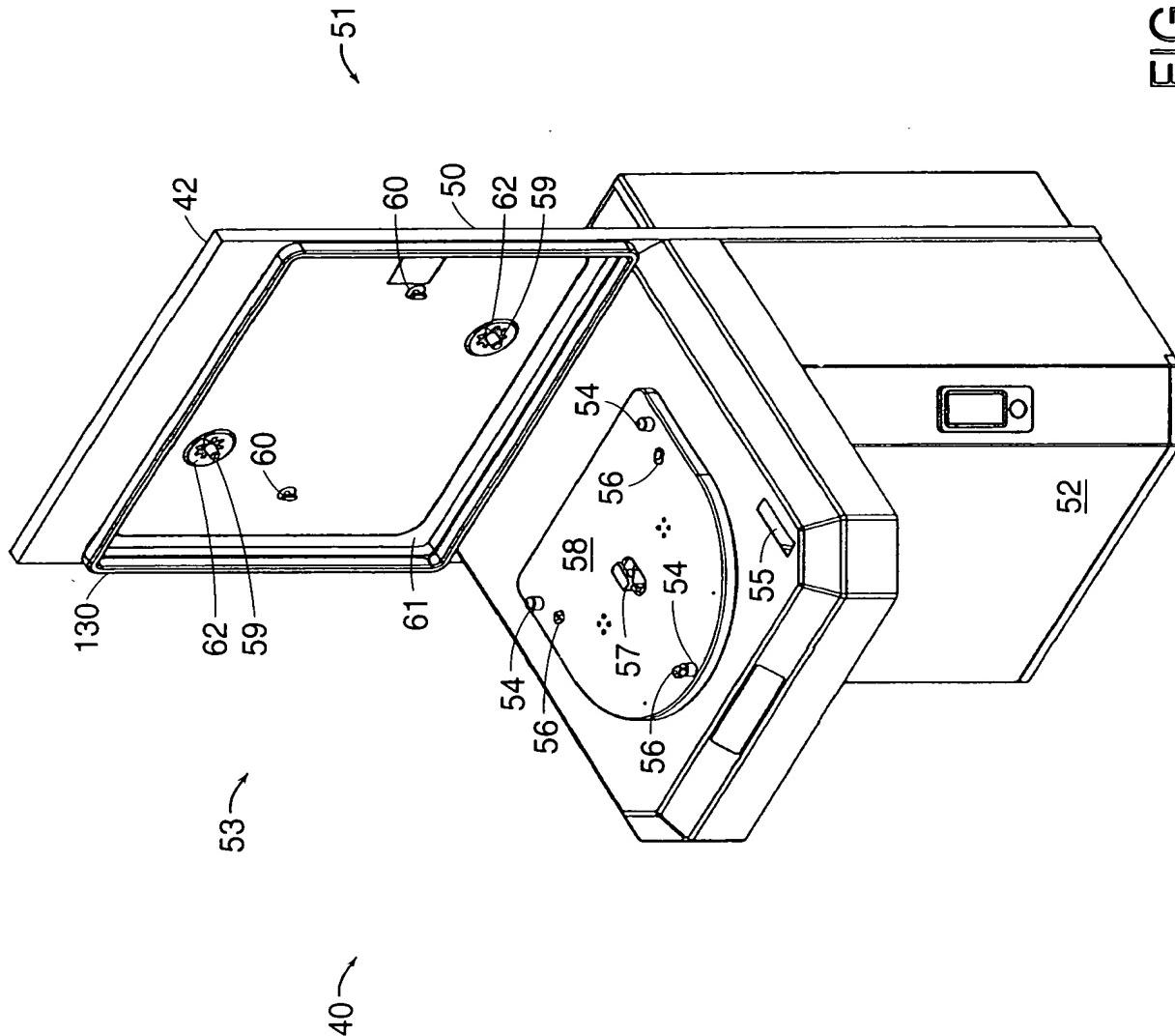
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FIG. 3



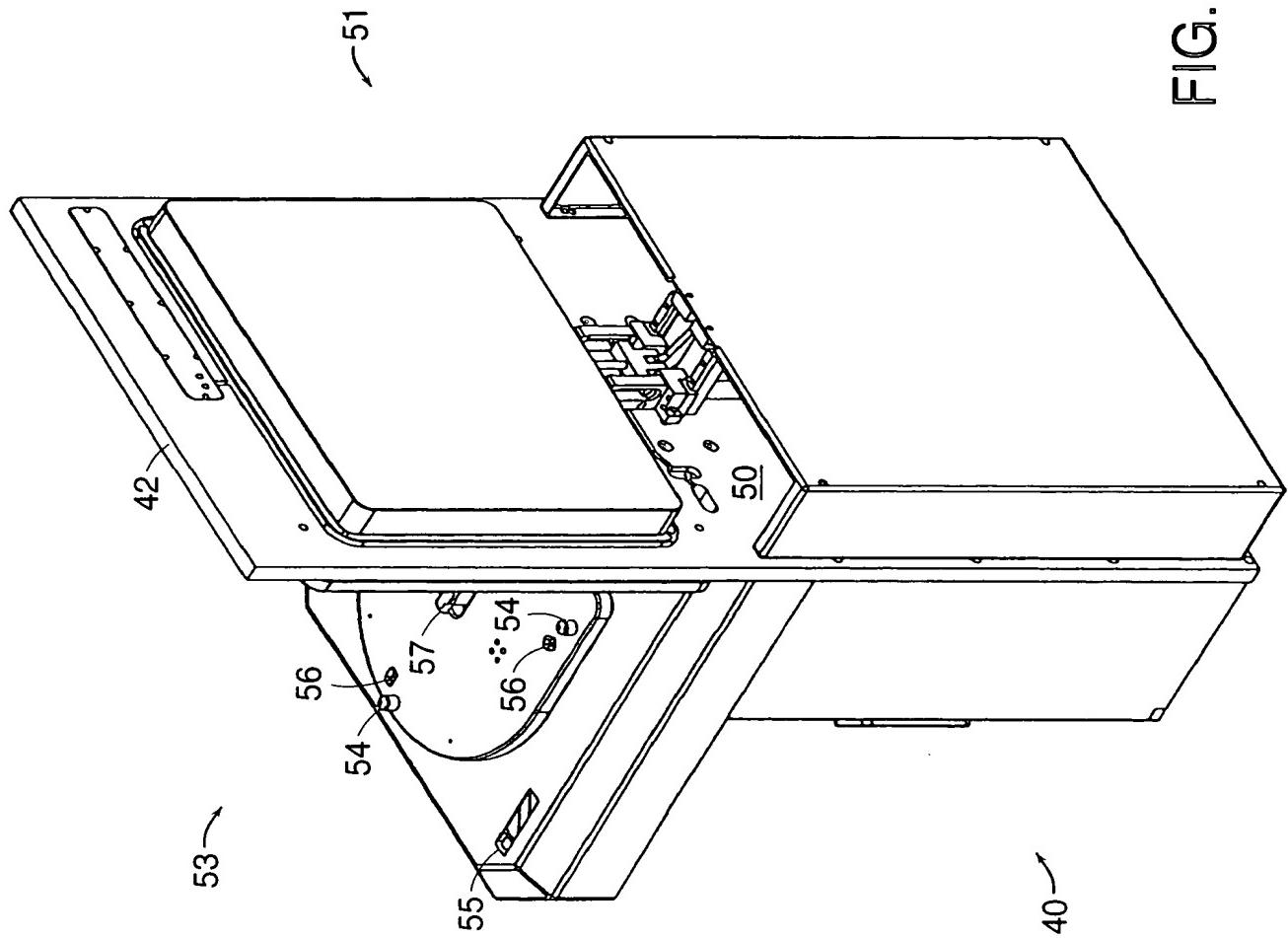
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FIG. 4



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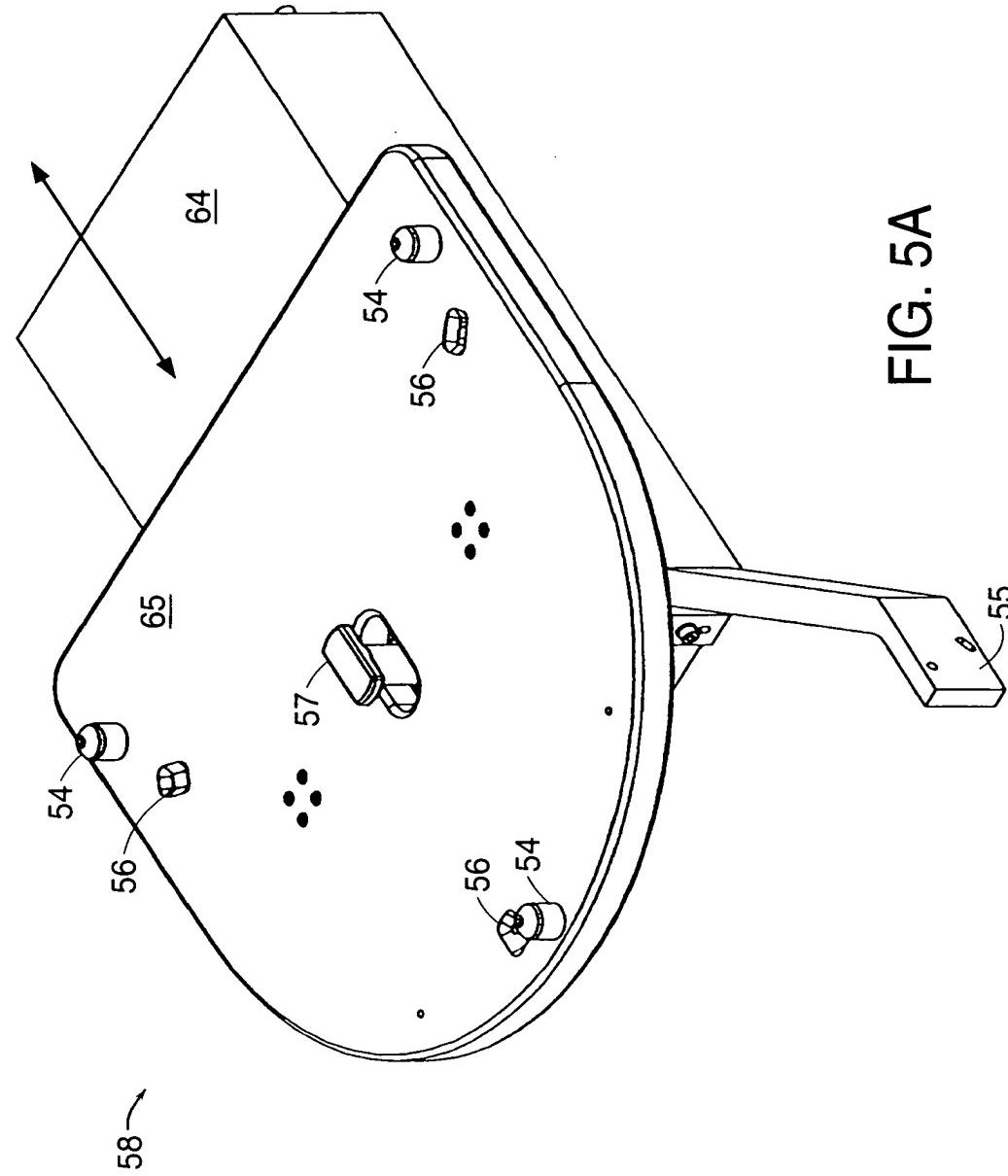
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FIG. 5A



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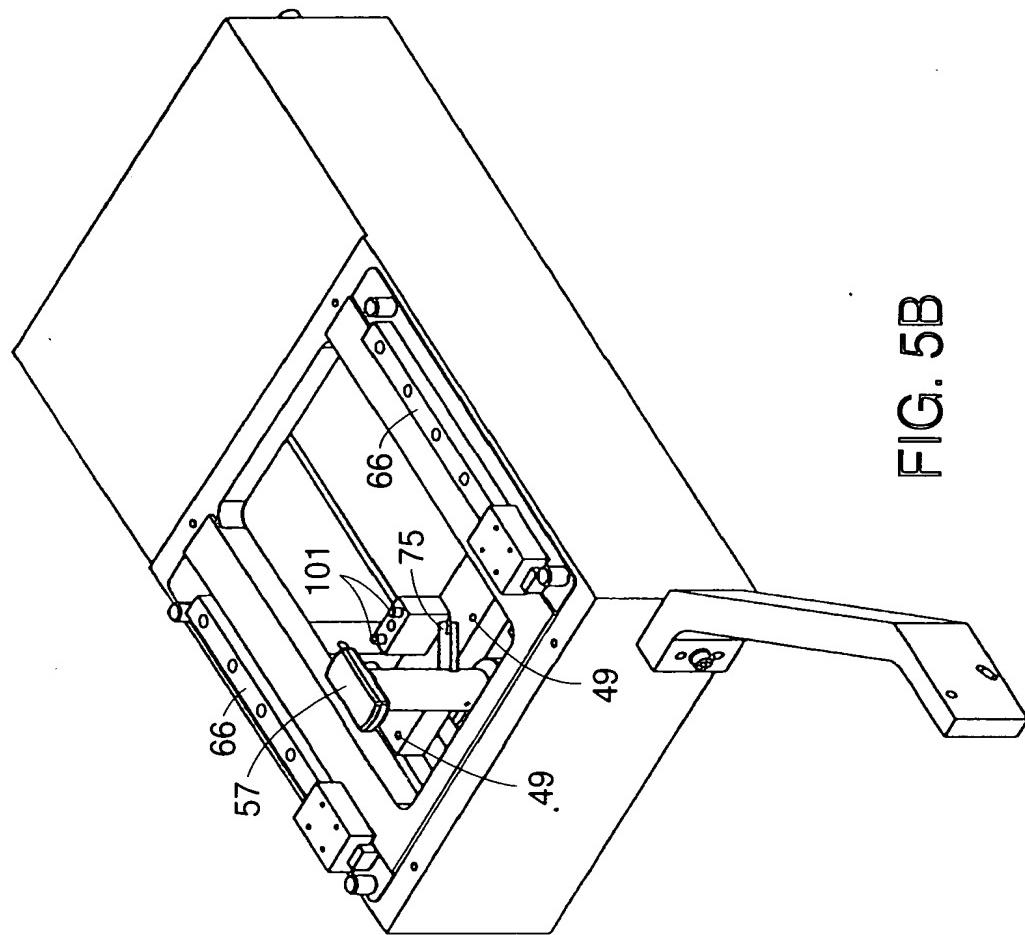


FIG. 5B

APPROVED	O.G. FIG.
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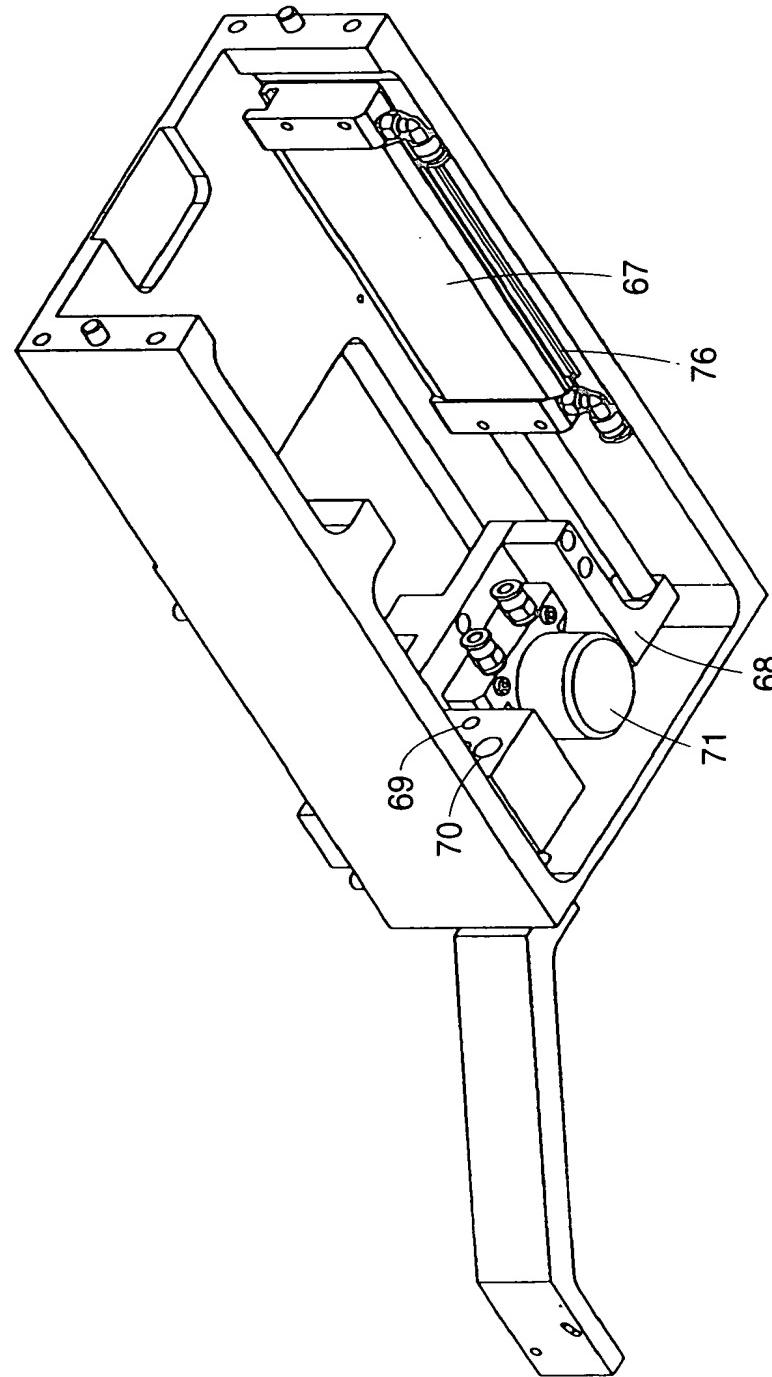


FIG. 5C

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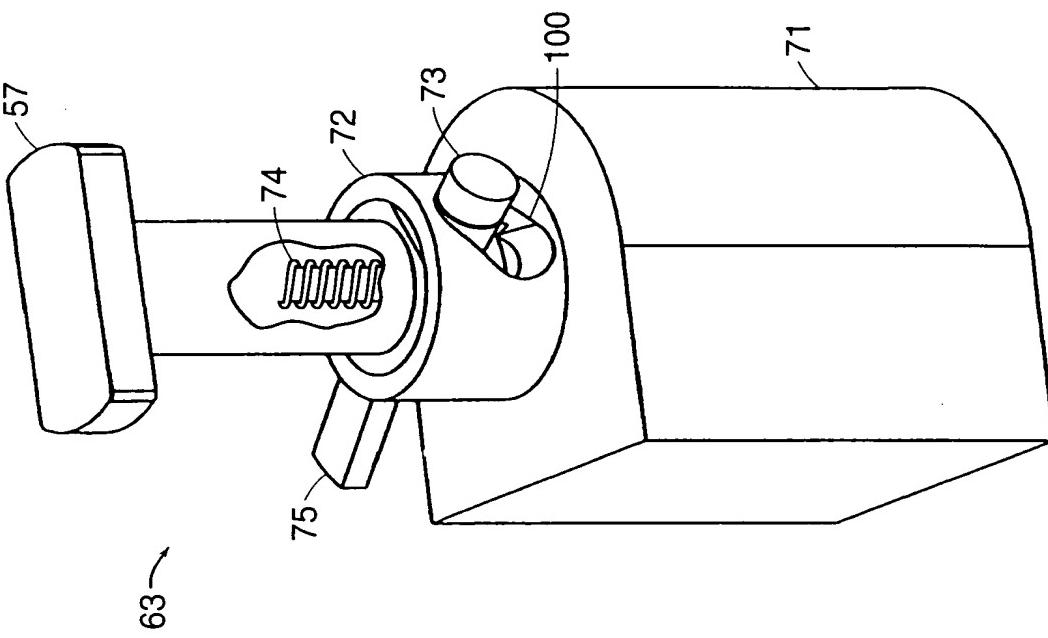


FIG. 5D

APPROVED	Q.G. FIG.
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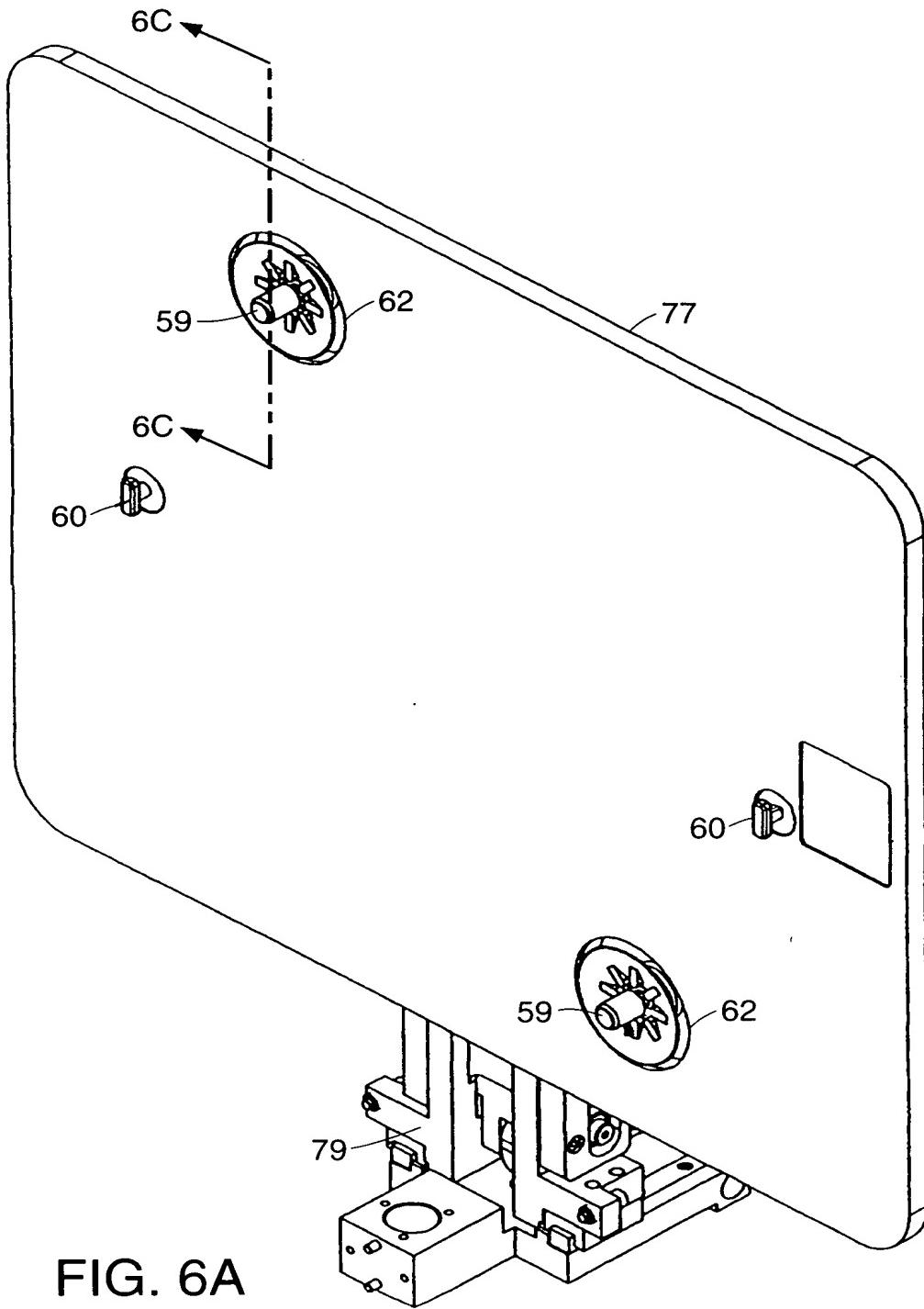


FIG. 6A

APPROVED	O.G. FIG.
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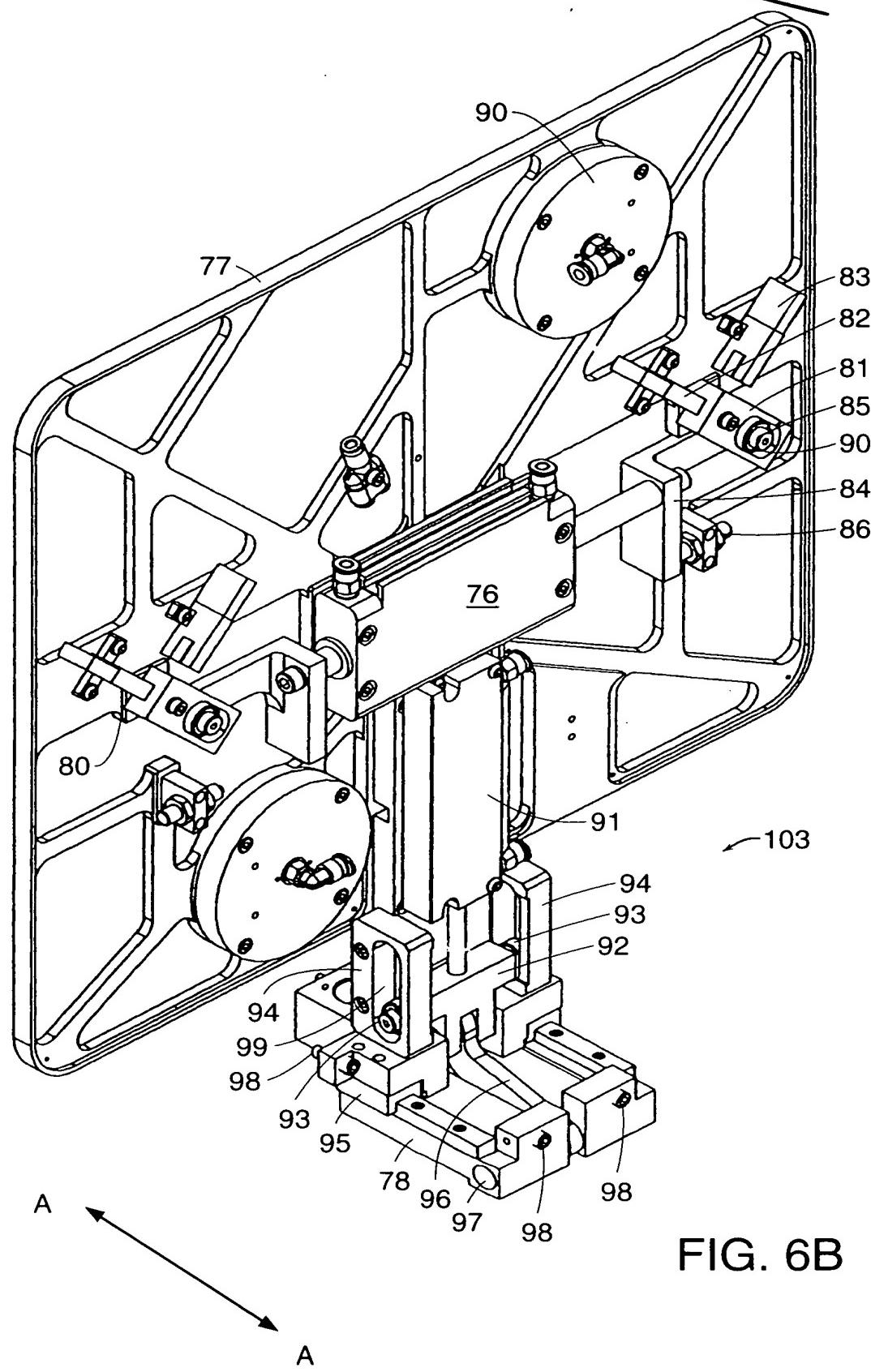


FIG. 6B

APPROVED	O.G. FIG.
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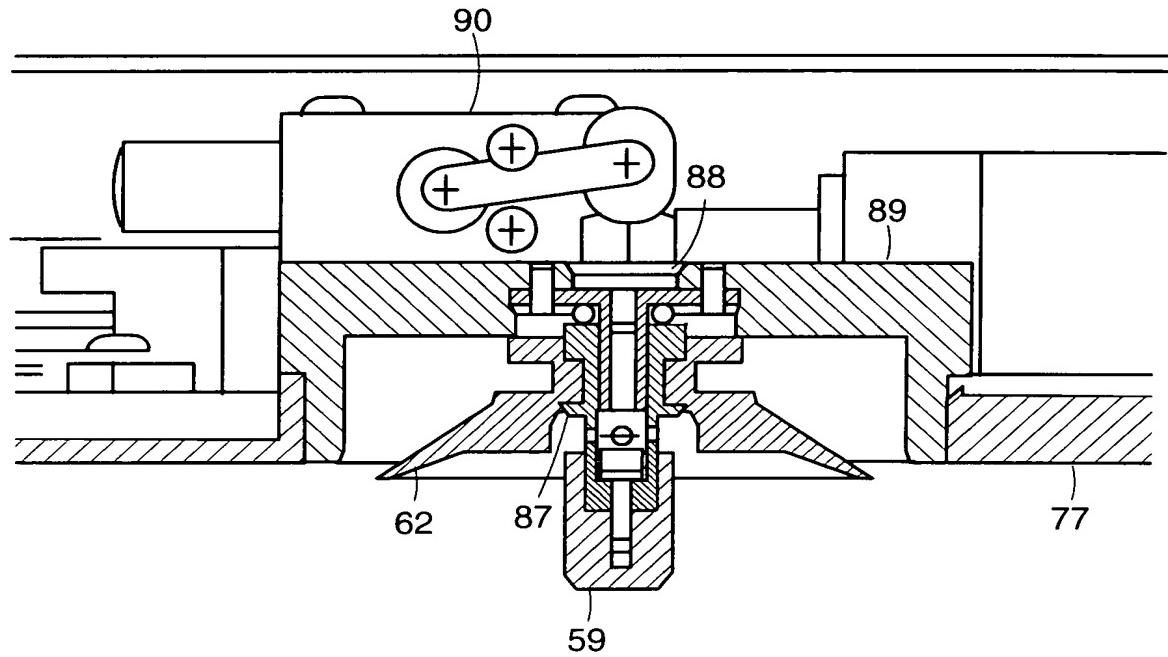


FIG. 6C

APPROVED	O.G. FIG.
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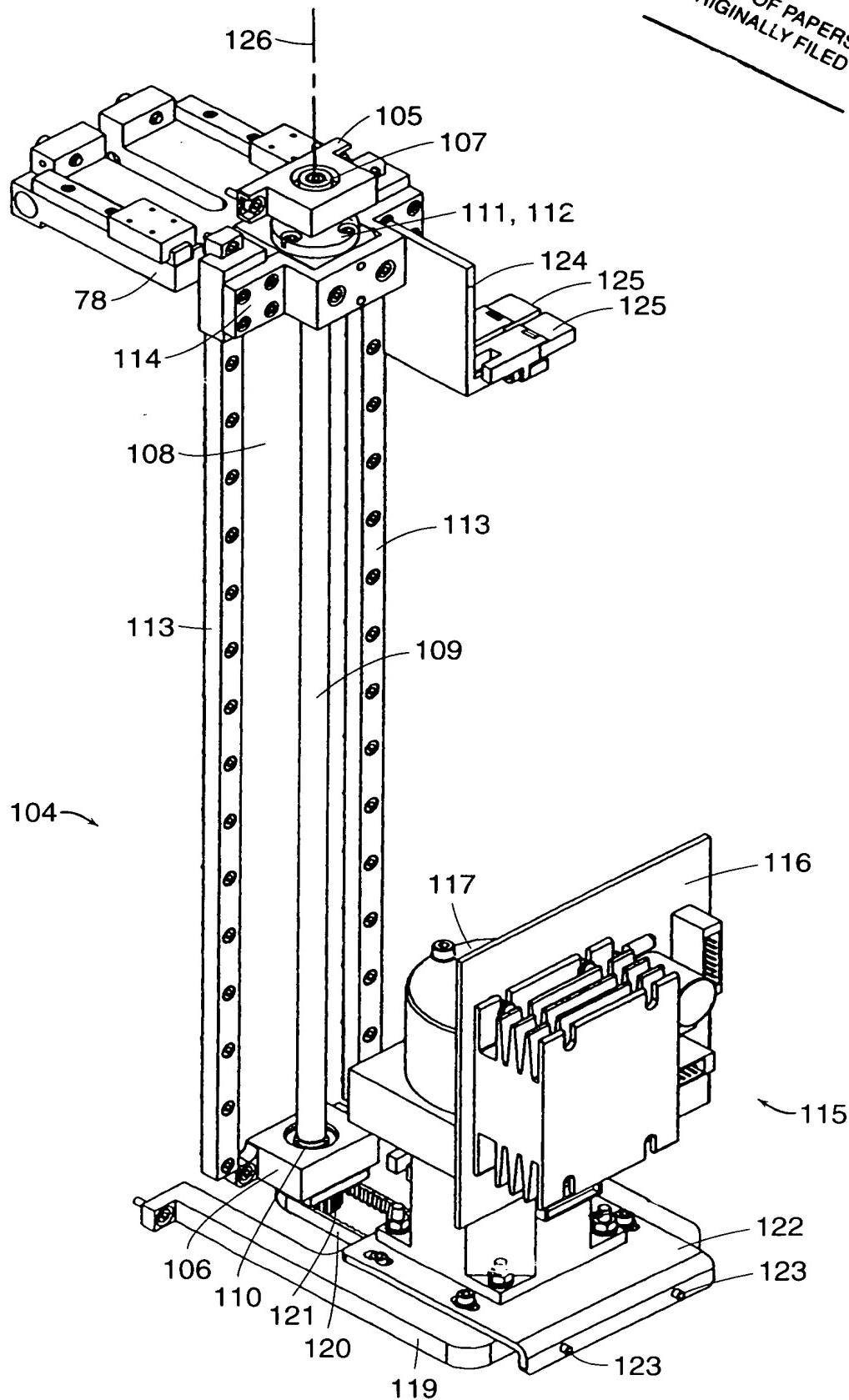


FIG. 7

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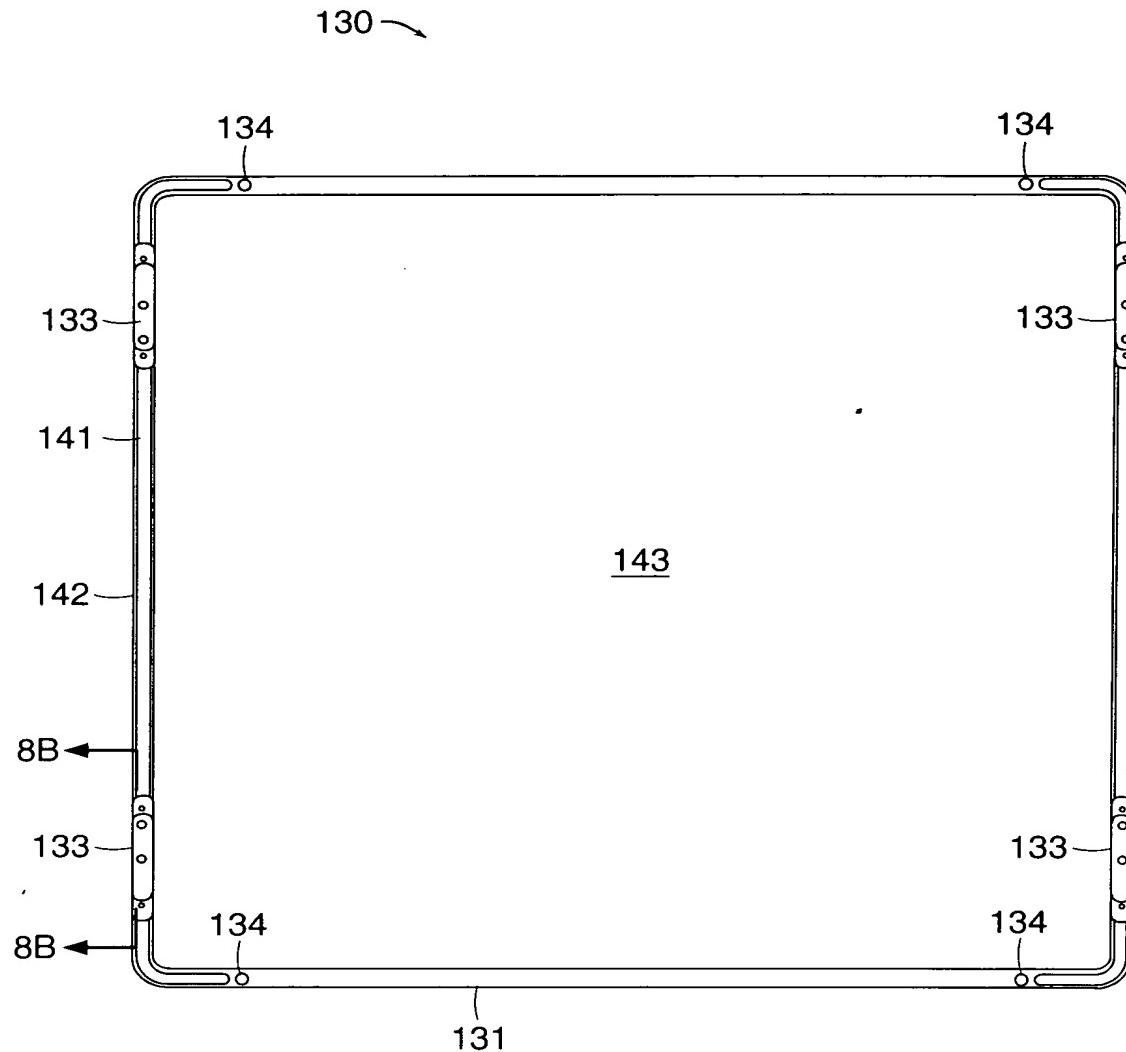


FIG. 8A

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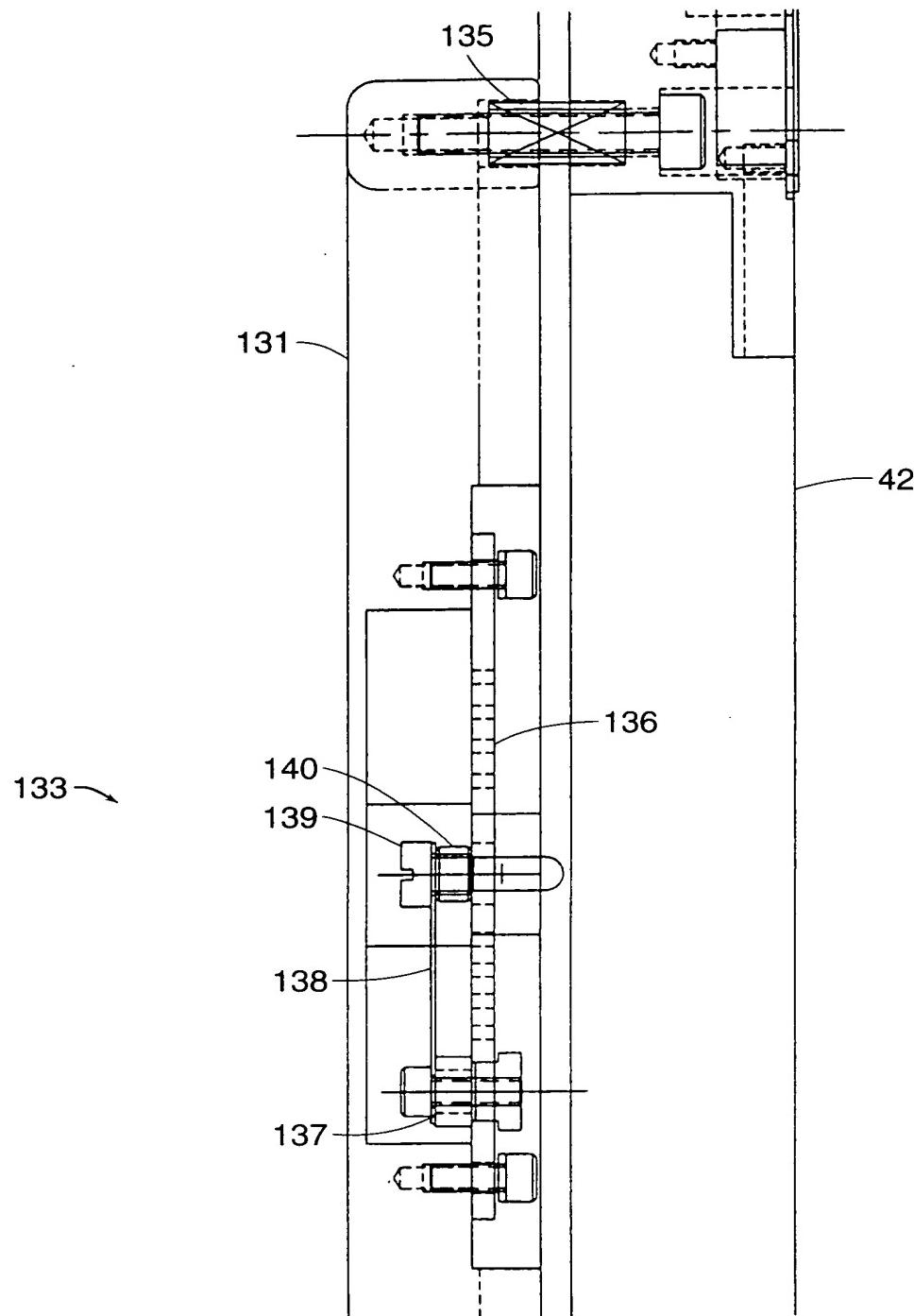


FIG. 8B

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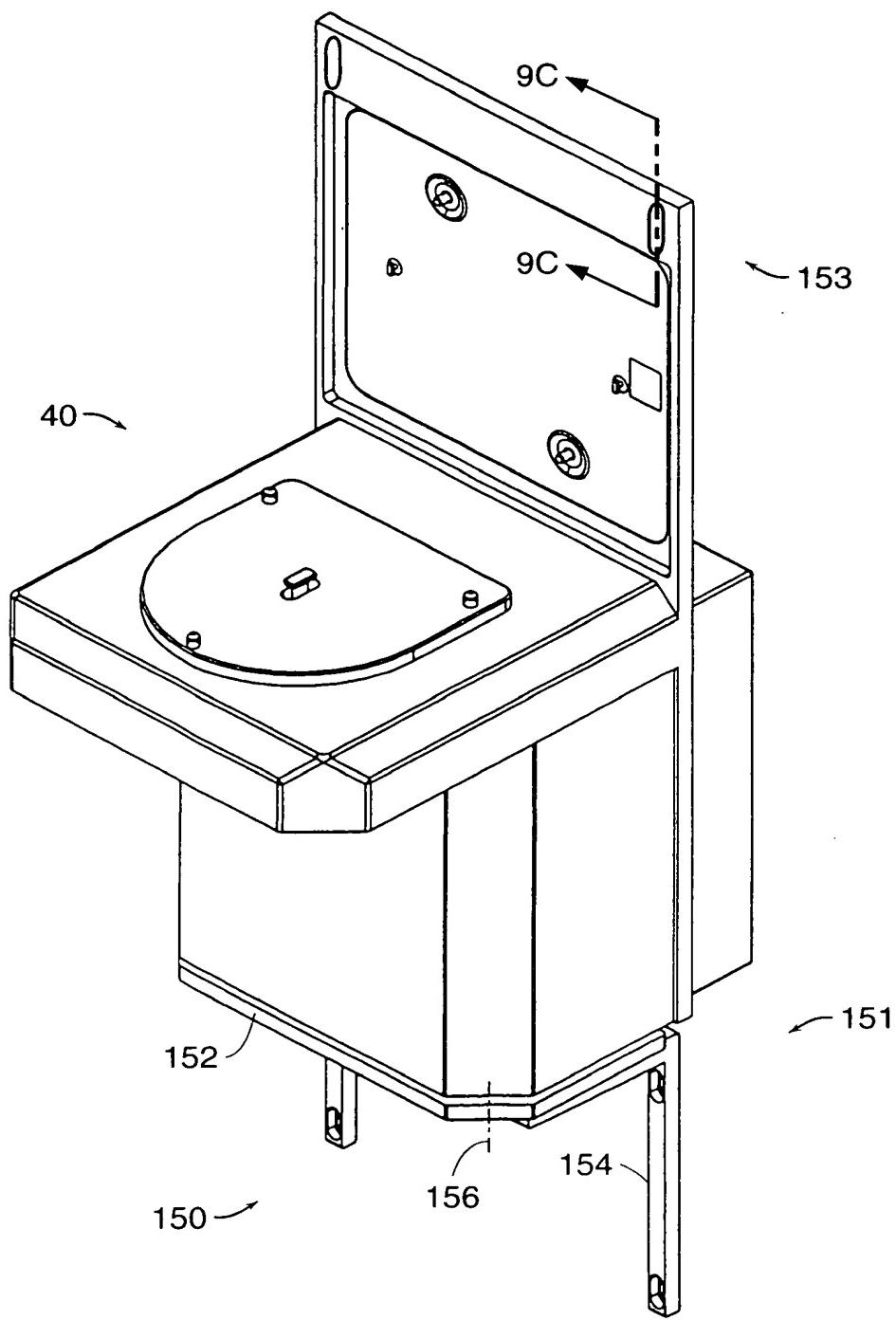


FIG. 9A

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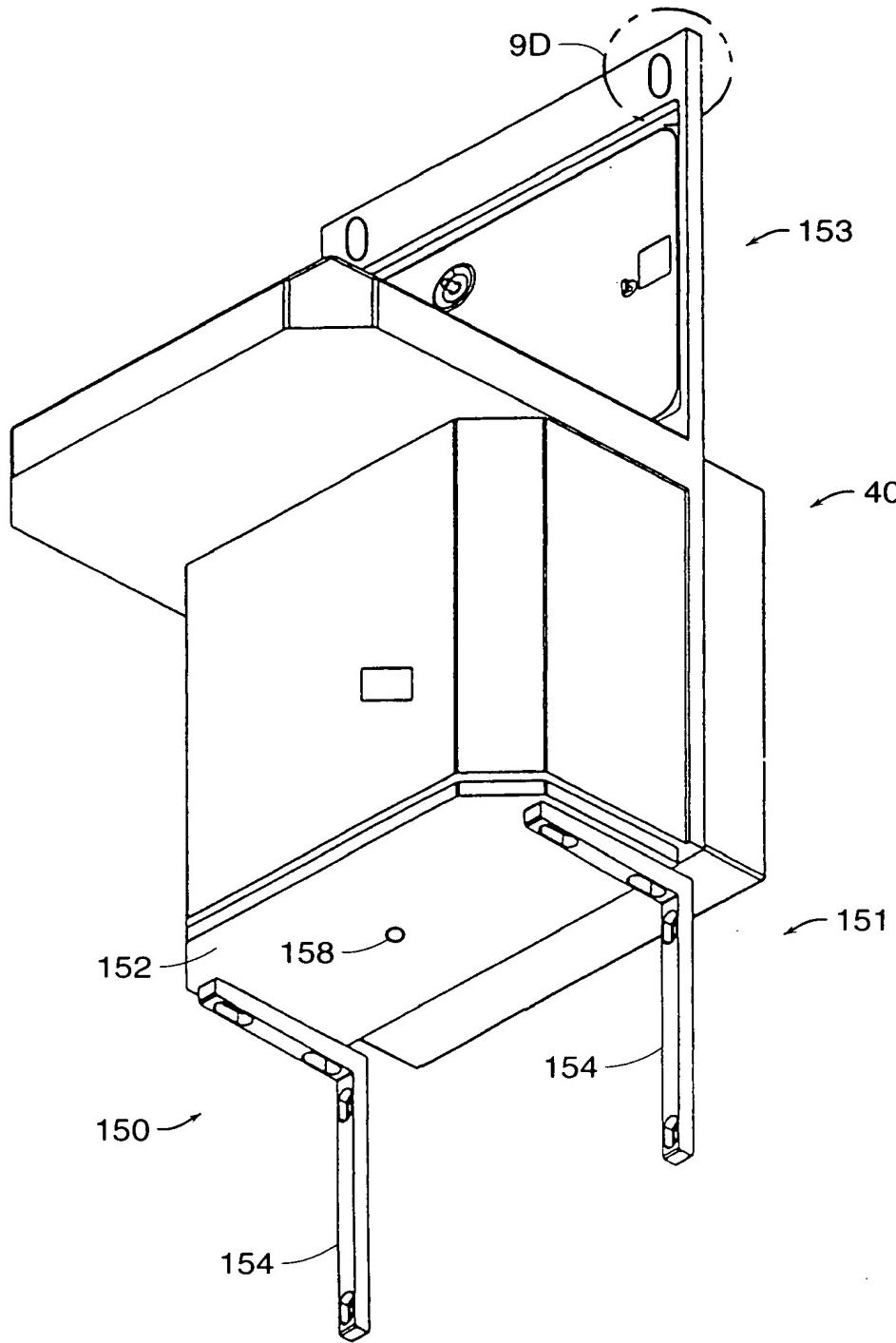


FIG. 9B

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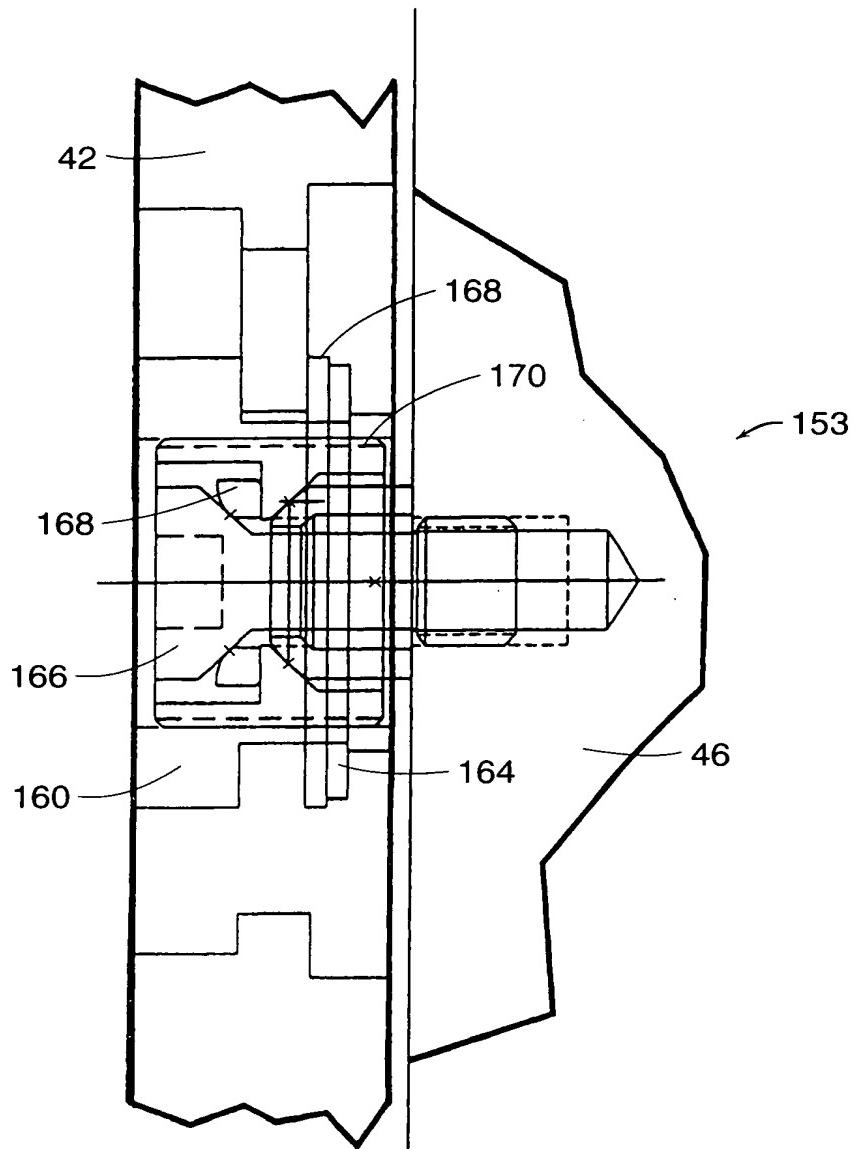


FIG. 9C

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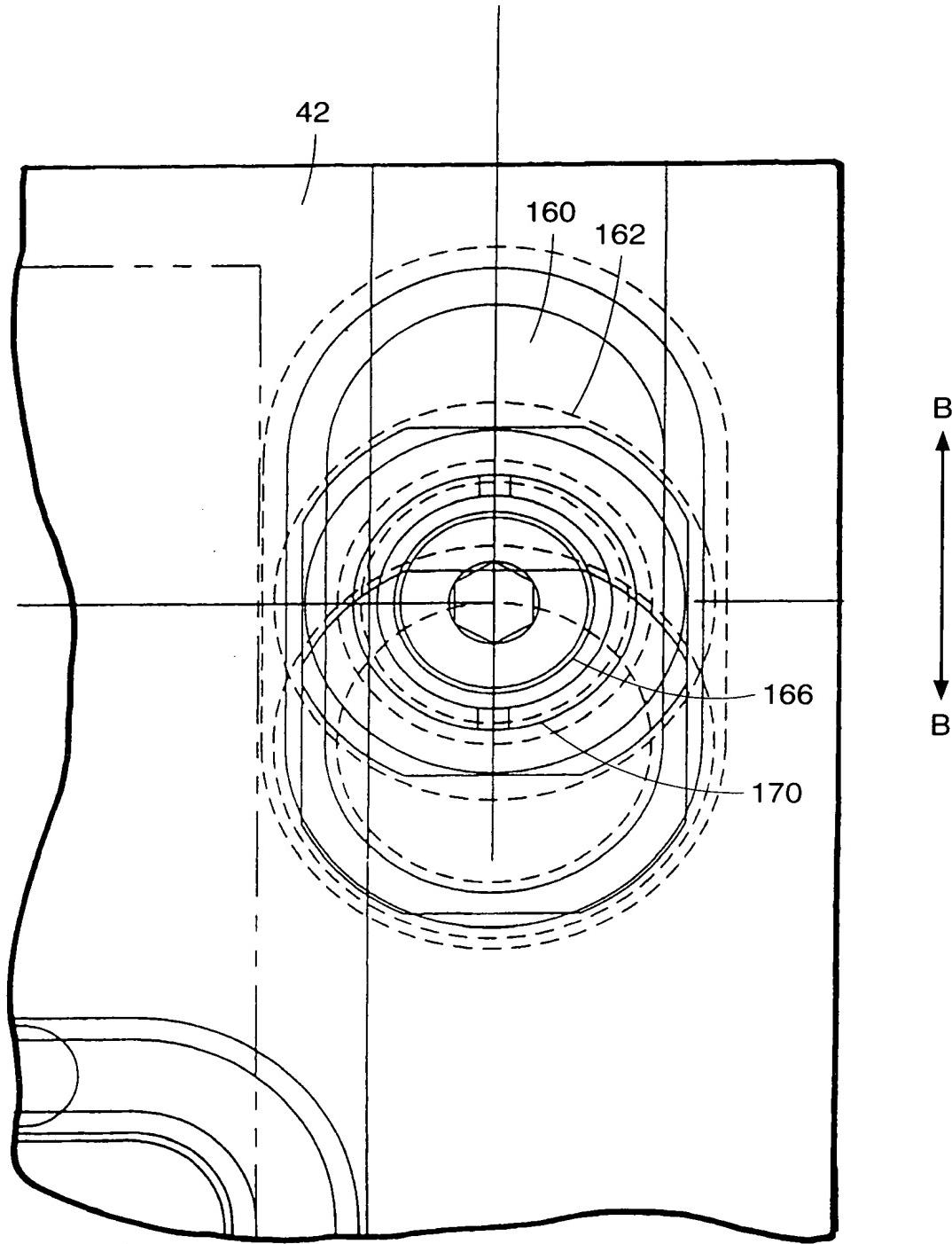


FIG. 9D

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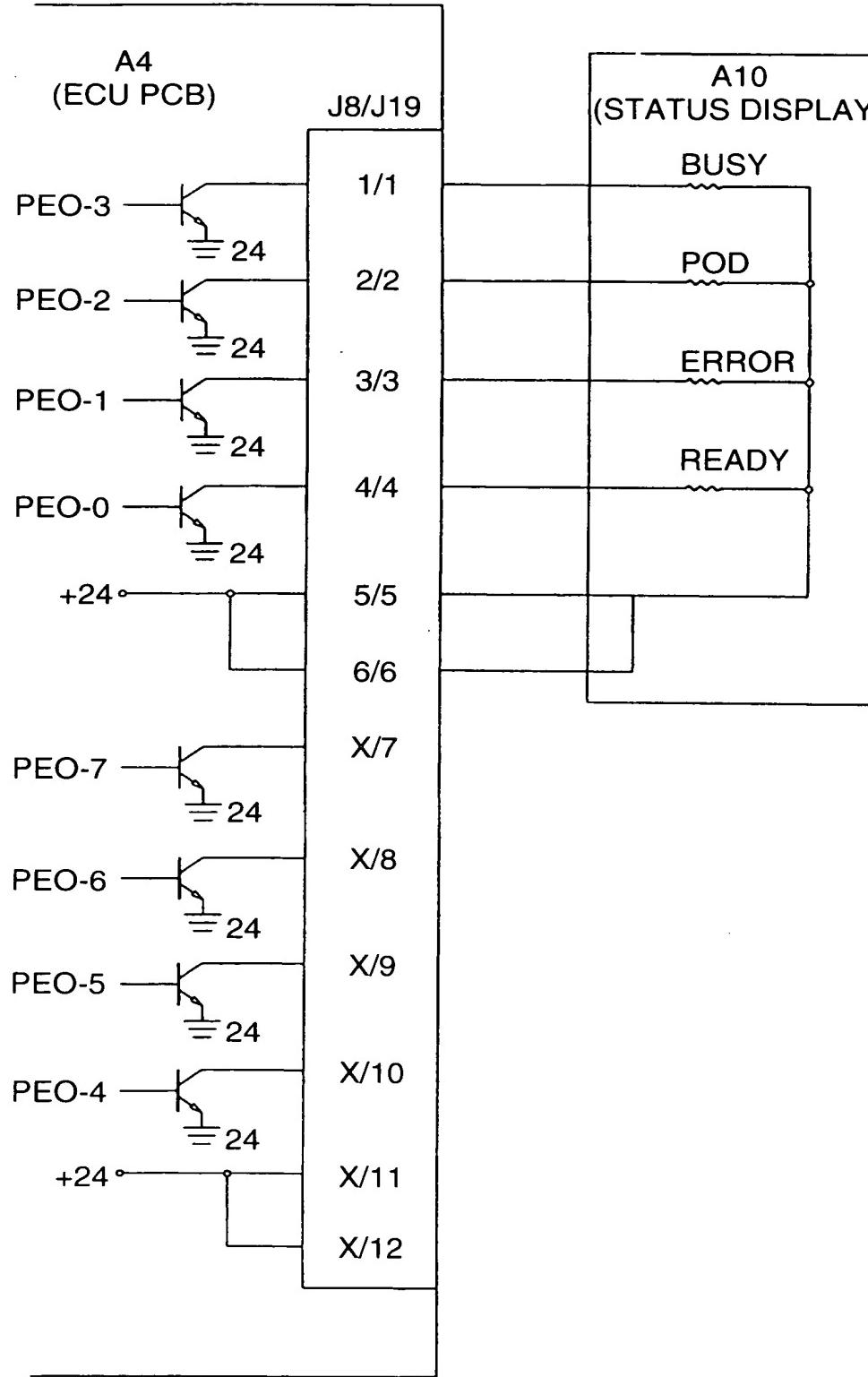


FIG. 10A

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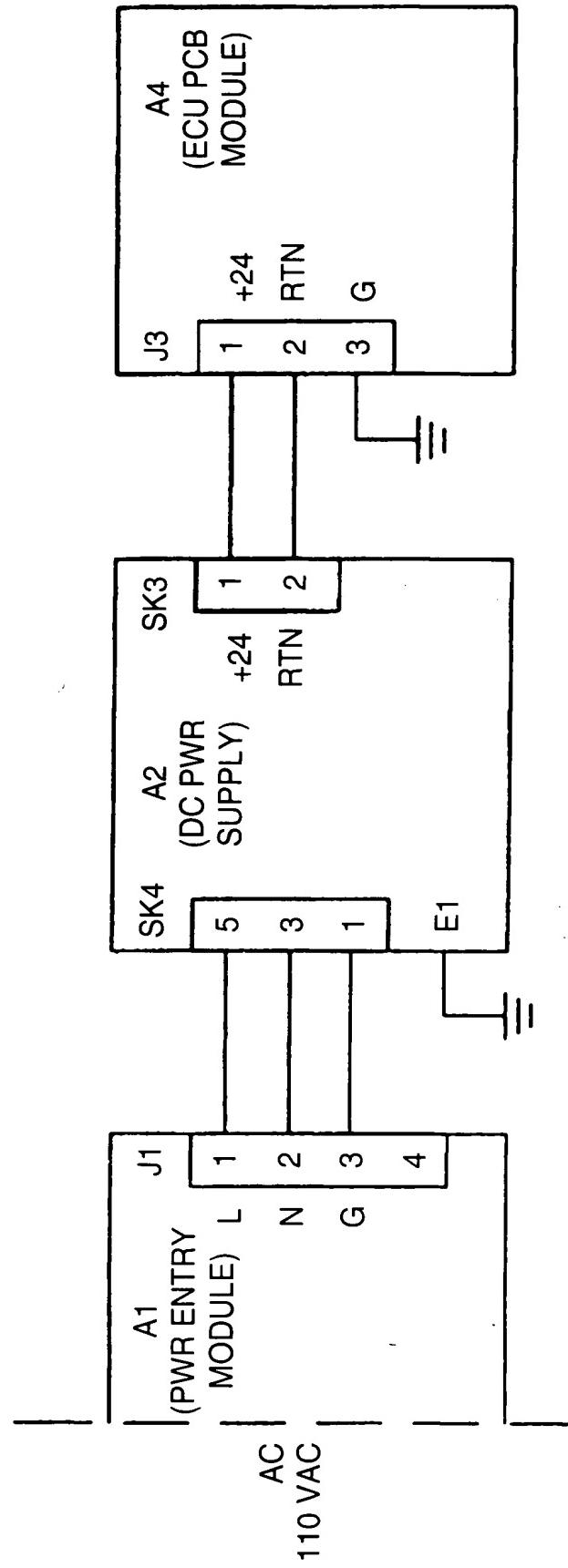


FIG. 10B

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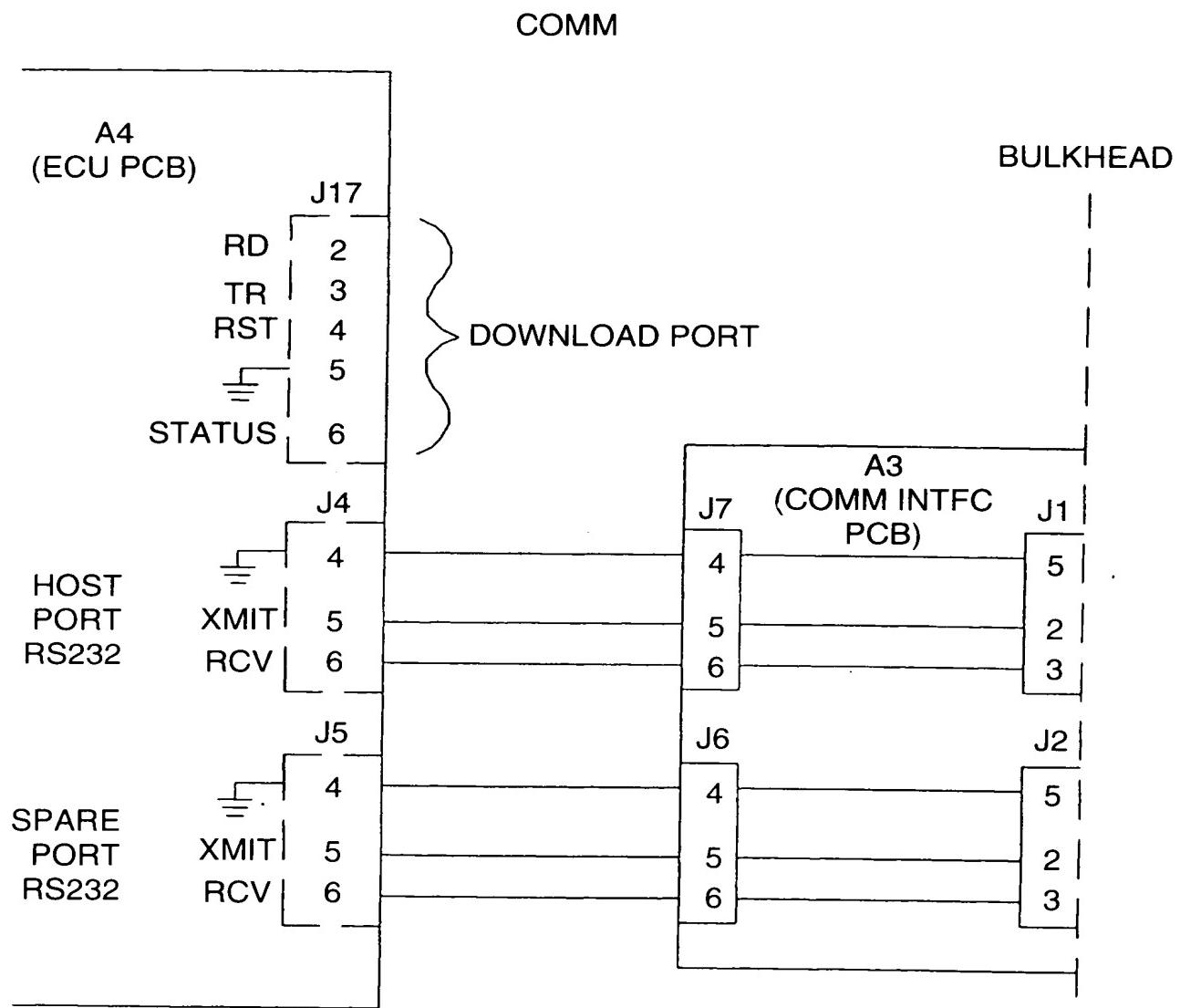
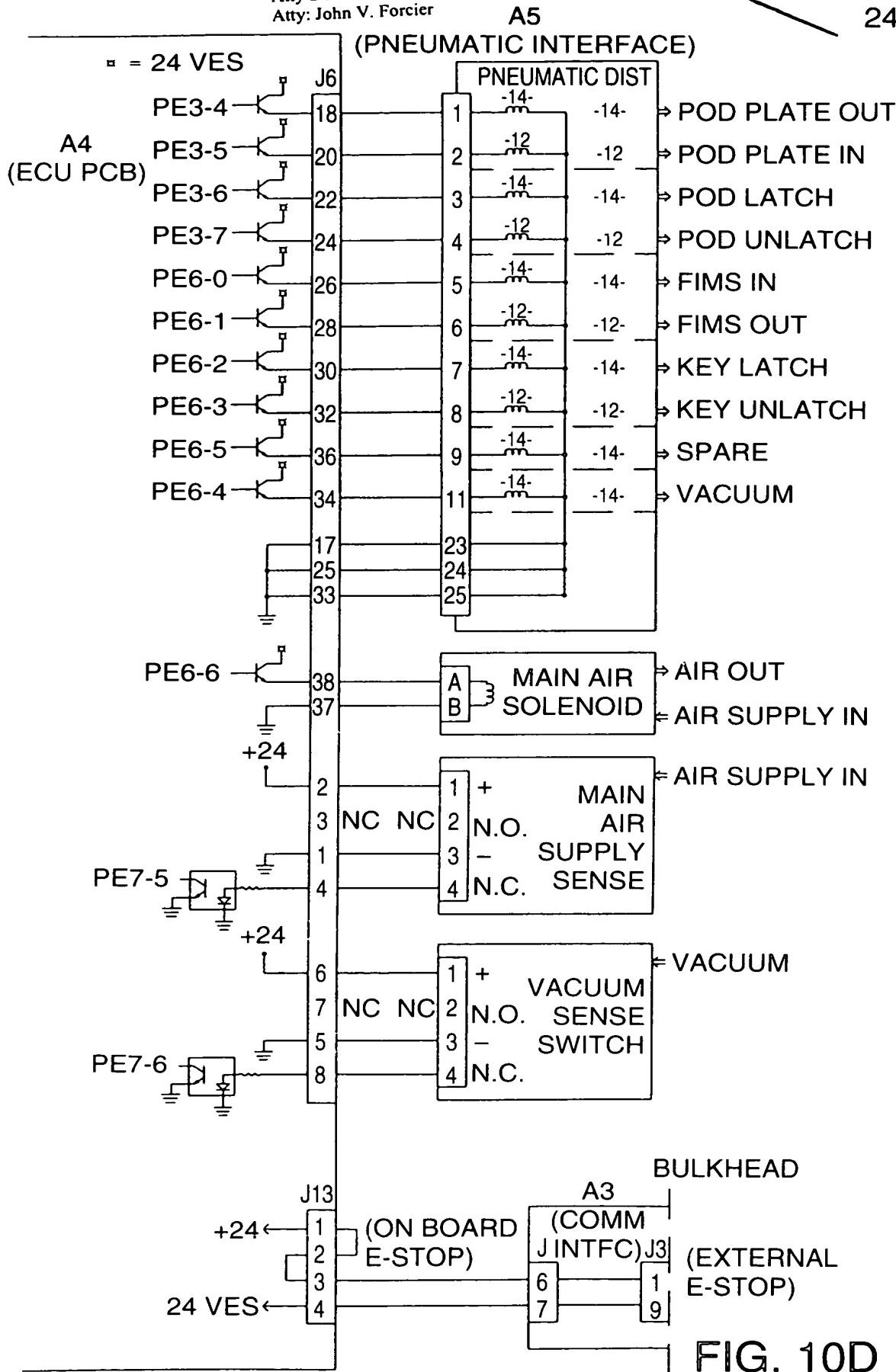


FIG. 10C

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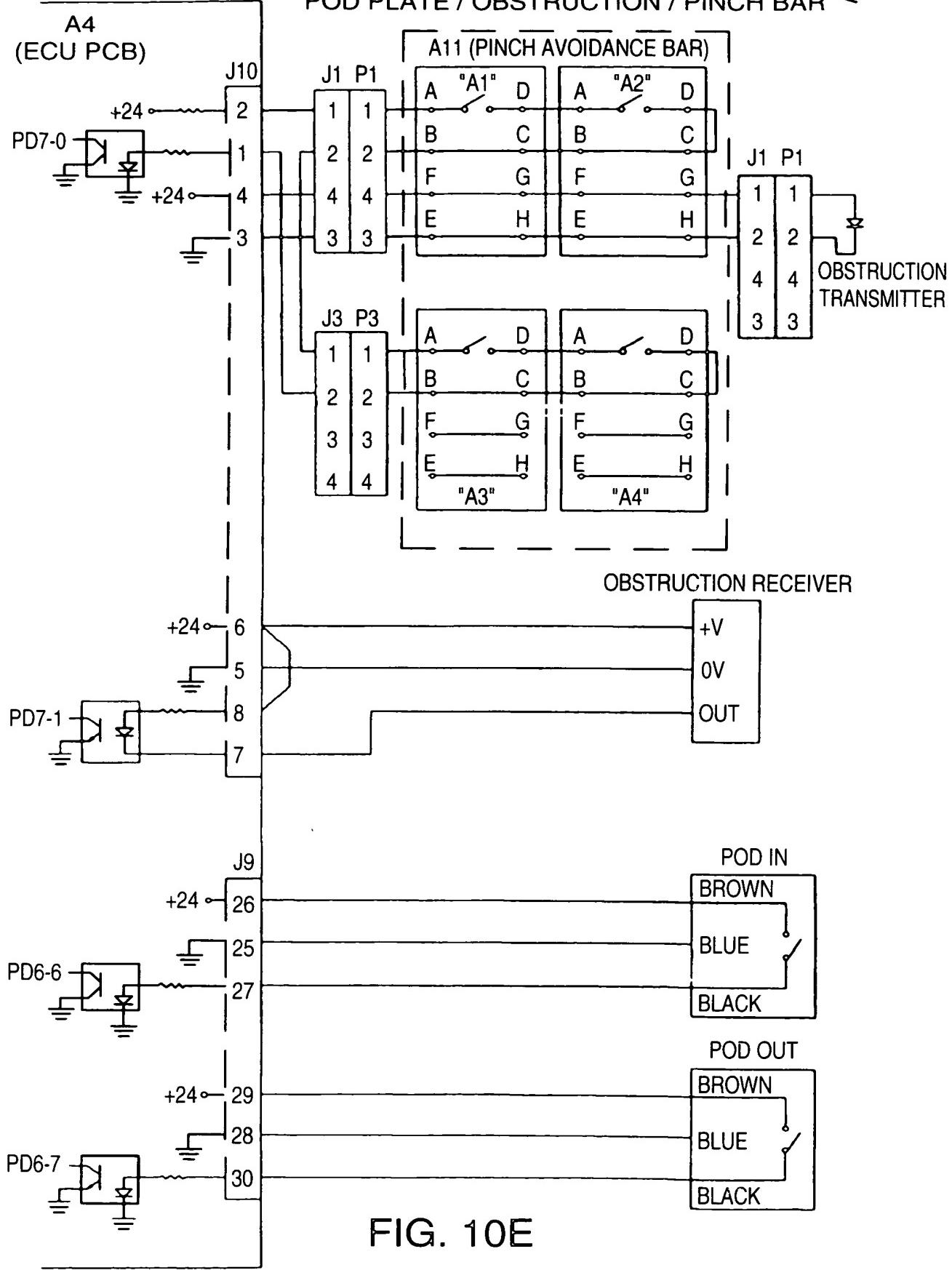
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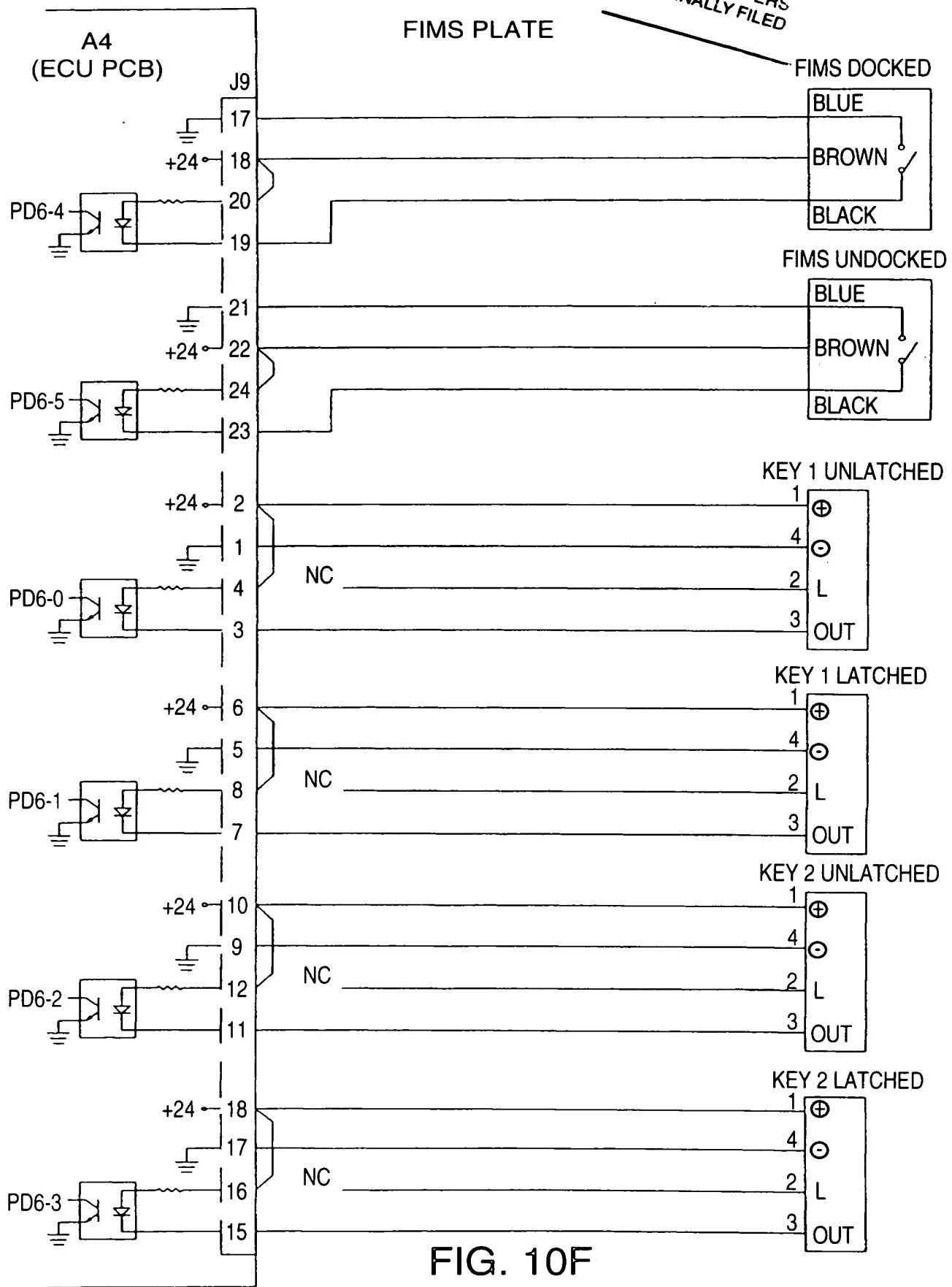


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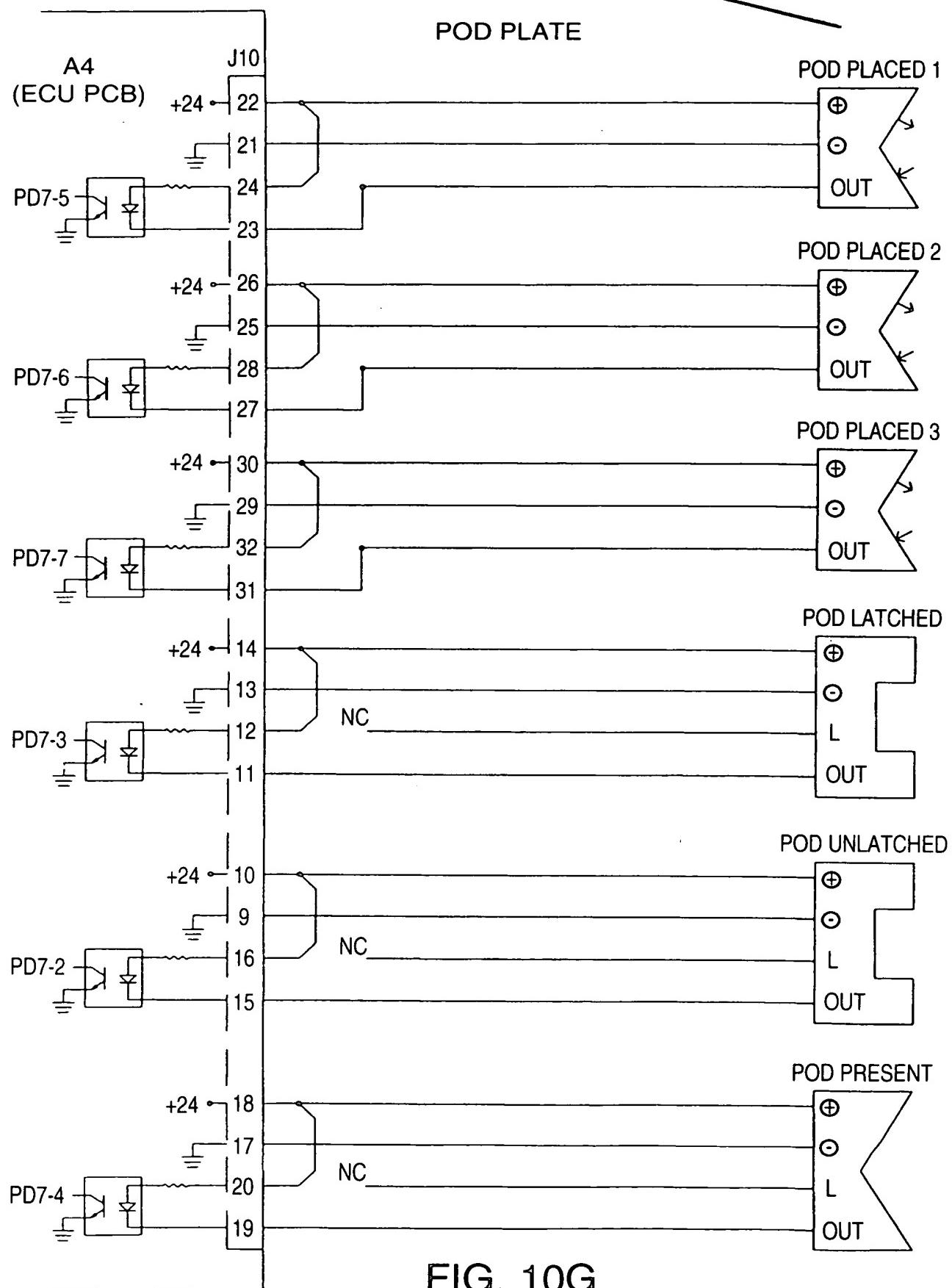


FIG. 10G

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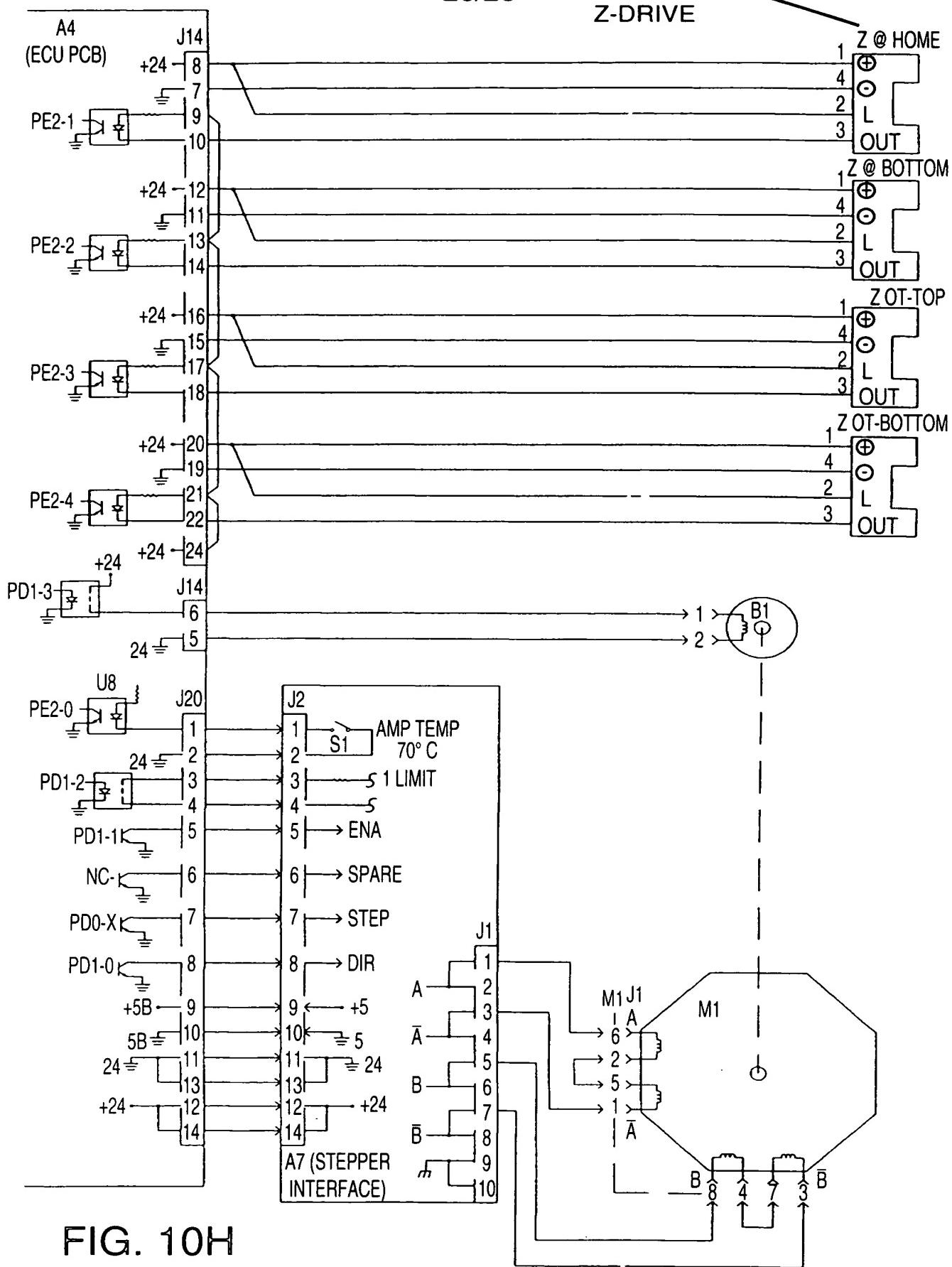


FIG. 10H